Weekly Progress Report

T12

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Week ending 03/29/2015

**Completed Tasks:**

1. Researched values and parts
2. Finish Writing Verilog code to test pattern generation
3. Re did eagle schematic parts from inches to mm to match Kris work
4. Updated experiment description

**Tasks to Be Completed Next Week:**

1. Review LSFR timing with Tony
2. After Tony approval finish LSFR schematic and route board

**Issues:**

Not getting as many patterns as theoretically predicted. Will meet with Tony